



1336 IMPACT User Manual (1336 IMPACT–5.0)

Please note that the following anomalies are present in the Version 1.01 firmware. Version 1.02 will correct these anomalies.

SCANport

In Version 1.01, if all SCANport devices are removed and a SCANport timeout fault exists, you cannot clear the fault from the L Option. To prevent the fault from occurring, clear the logic mask for the device before removing the device or turn of the fault in *Fault Select 1* (parameter 20) bits 9 through 14.

In Version 1.02, you can clear the fault through the L Option.

In Version 1.01, the clear fault bit for logic input on the gateway may have to be transitioned multiple times before the faults are cleared. This has been fixed in Version 1.02.

Drive/Inverter Status Command Direction Bit and Rotating Direction Bit

In Version 1.01, the operation of bit 2 in *Drive/Inv Status* (parameter 15) may be confusing. Bit 2, Command Dir, shows which direction (forward or reverse) has been requested. If bit 11 in *Logic Options* (parameter 17) is set indicating that a bipolar reference is selected, the command direction bit (bit 2 in *Drive/Inv Status*) is always set, indicating a forward command direction. The command direction bit remains in the forward state, regardless of the sign of the speed reference input.

When you use a negative speed reference to run the motor in reverse, bit 2 (Command Dir) remains in the forward (set) state, but bit 3 (Rotating Dir) goes to the reverse (clear) state. This results in confusing status displays on the Human Interface Module (HIM) and Graphic Programming Terminal (GPT) terminals. On the HIM, the reverse direction LED will blink. On the GPT, the direction information is displayed as “FORWARD” blinking text.

In Version 1.02, the operation of bits 2 and 3 in *Drive/Inv Status* was changed. Bit 2 (Command Dir) is set for positive or zero values of *Command Spd Sts* (parameter 82) and clear for negative values. Similarly, bit 3 (Rotating Dir) is set for positive or zero values of *Motor Speed* (parameter 81) and clear for negative values.